

L Number	Hits	S arch T xt	DB	Tim stamp
1	4002	250/214A.ccls. 250/214R.ccls. 327/514.ccls.	USPAT; EPO; JPO; DERWENT	2003/04/29 09:08
6	409099	light n ar5 (rec iv\$4 or rec pt\$4 or detect\$4 or sens\$4)	USPAT; EPO; JPO; DERWENT	2003/04/29 09:10
11	4926	(amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)	USPAT; EPO; JPO; DERWENT	2003/04/29 11:09
16	521	(light near5 (receiv\$4 or recept\$4 or detect\$4 or sens\$4)) and ((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:13
21	14	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) and ((light near5 (receiv\$4 or recept\$4 or detect\$4 or sens\$4)) and ((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:15
26	7331	jitter\$4 near5 (control\$5 or minimiz\$8 or reduc\$5)	USPAT; EPO; JPO; DERWENT	2003/04/29 09:29
31	574	(data near5 (reshap\$5 or regenerat\$6)) and (clock near4 recover\$5)	USPAT; EPO; JPO; DERWENT	2003/04/29 09:59
36	75	(jitter\$4 near5 (control\$5 or minimiz\$8 or reduc\$5)) and ((data near5 (reshap\$5 or regenerat\$6)) and (clock near4 recover\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:31
41	0	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) and ((jitter\$4 near5 (control\$5 or minimiz\$8 or reduc\$5)) and ((data near5 (reshap\$5 or regenerat\$6)) and (clock near4 recover\$5)))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:31
46	65604	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) nd ((data near5 (reshap\$5 or regenerat\$6)) and (clock near4 recover\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:31
51	0	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) and ((data near5 (reshap\$5 or regenerat\$6)) and (clock near4 recover\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:31
56	9	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) and (jitter\$4 near5 (control\$5 or minimiz\$8 or reduc\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:34
61	1	((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and ((data near5 (reshap\$5 or regenerat\$6)) and (clock near4 recover\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:51
71	0	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) and (((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and (jitter\$4 n ar5 (c ntr l\$5 r minimiz\$8 r reduc\$5)))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:52
66	32	((amplif\$8 r pr amplif\$8 r AC) n ar8 (DC near3 bias)) and (jitter\$4 near5 (c ntr l\$5 r minimiz\$8 or r duc\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:59

76	0	((light n ar5 (rec iv\$4 or r c pt\$4 or d t ct\$4 or s ns\$4)) and ((amplif\$8 r pr amplif\$8 r AC) n ar8 (DC near3 bias))) and ((jitt r\$4 n ar5 (control\$5 r minimiz\$8 or r duc\$5)) and ((data n ar5 (reshap\$5 r reg n rat\$6)) and (cl ck n ar4 r cover\$5)))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:59
81	0	((light near5 (receiv\$4 or recept\$4 or detect\$4 or sens\$4)) and ((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias))) and ((data near5 (reshap\$5 or regenerat\$6)) and (clock near4 recover\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 09:59
86	9162	clock near4 recover\$5	USPAT; EPO; JPO; DERWENT	2003/04/29 09:59
91	2	((light near5 (receiv\$4 or recept\$4 or detect\$4 or sens\$4)) and ((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias))) and (clock near4 recover\$5)	USPAT; EPO; JPO; DERWENT	2003/04/29 10:00
96	3	subtract\$4 near5 circuit\$2	USPAT; EPO; JPO; DERWENT	2003/04/29 10:01
101	352496	(data or clock) near5 circuit\$5	USPAT; EPO; JPO; DERWENT	2003/04/29 10:38
106	32621	subtract\$4 near5 circuit\$2	USPAT; EPO; JPO; DERWENT	2003/04/29 10:02
111	9951	((data or clock) near5 circuit\$5) and (subtract\$4 near5 circuit\$2)	USPAT; EPO; JPO; DERWENT	2003/04/29 10:02
116	5	((light near5 (receiv\$4 or recept\$4 or detect\$4 or sens\$4)) and ((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias))) and (((data or clock) near5 circuit\$5) and (subtract\$4 near5 circuit\$2))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:04
121	39	((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and (((data or clock) near5 circuit\$5) and (subtract\$4 near5 circuit\$2))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:06
126	1	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) and (((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and (((data or clock) near5 circuit\$5) and (subtract\$4 near5 circuit\$2)))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:15
131	0	((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and (((data or clock) near5 circuit\$5) and (subtract\$4 near5 circuit\$2))) and ((data near5 (reshap\$5 or regenerat\$6)) and (clock near4 recover\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:15
136	4	((amplif\$8 or preamplif\$8 r AC) n ar8 (DC near3 bias)) and (((data r cl k) n ar5 circuit\$5) and (subtra t\$4 n ar5 circuit\$2))) and (jitter\$4 near5 (c ntr l\$5 r minimiz\$8 or r duc\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:36

141	501	((amplif\$8 r pr amplif\$8 or AC) n ar8 (DC n ar3 bias)) and ((data or clock) n ar5 circuit\$5)	USPAT; EPO; JPO; DERWENT	2003/04/29 10:36
151	0	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) and ((light n ar5 (r ceiv\$4 or recept\$4 or d t ct\$4 or s ns\$4)) and (((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and ((data or clock) near5 circuit\$5)))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:36
146	104	(light near5 (receiv\$4 or recept\$4 or detect\$4 or sens\$4)) and (((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and ((data or clock) near5 circuit\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:36
156	0	(jitter\$4 near5 (control\$5 or minimiz\$8 or reduc\$5)) and ((light near5 (receiv\$4 or recept\$4 or detect\$4 or sens\$4)) and (((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and ((data or clock) near5 circuit\$5)))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:37
161	104	((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and ((light near5 (receiv\$4 or recept\$4 or detect\$4 or sens\$4)) and (((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and ((data or clock) near5 circuit\$5)))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:37
166	2050	(data and clock) near5 circuit\$5	USPAT; EPO; JPO; DERWENT	2003/04/29 11:12
171	0	((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and ((light near5 (receiv\$4 or recept\$4 or detect\$4 or sens\$4)) and (((amplif\$8 or preamplif\$8 or AC) near8 (DC near3 bias)) and ((data or clock) near5 circuit\$5)))) and ((data and clock) near5 circuit\$5)	USPAT; EPO; JPO; DERWENT	2003/04/29 10:46
176	192	(jitter\$4 near5 (control\$5 or minimiz\$8 or reduc\$5)) and (((data or clock) near5 circuit\$5) and (subtract\$4 near5 circuit\$2))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:46
181	0	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) and ((jitter\$4 near5 (control\$5 or minimiz\$8 or reduc\$5)) and (((data or clock) near5 circuit\$5) and (subtract\$4 near5 circuit\$2)))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:46
186	32	(clock near4 recover\$5) and ((jitter\$4 near5 (control\$5 or minimiz\$8 or reduc\$5)) and (((data or clock) near5 circuit\$5) and (subtract\$4 near5 circuit\$2)))	USPAT; EPO; JPO; DERWENT	2003/04/29 10:58
191	39249	DC near5 (detect\$4 or sens\$4 or monitor\$4)	USPAT; EPO; JPO; DERWENT	2003/04/29 10:59
196	190322	(correct\$5 or adjust\$5) n ar5 circuit\$2	USPAT; EPO; JPO; DERWENT	2003/04/29 11:02

201	5231	(DC near5 (detect\$4 or sense\$4 or monitor\$4)) and ((correct\$5 or adjust\$5) near5 circuit\$2)	USPAT; EPO; JPO; DERWENT	2003/04/29 11:03
211	1	(250/214A.ccls. 250/214R.ccls. 327/514.ccls.) and (((light near5 (receive\$4 or receipt\$4 or detect\$4 or sense\$4)) and ((amplifier\$8 or preamplifier\$8 or AC) near8 (DC near3 bias))) and ((DC near5 (detect\$4 or sense\$4 or monitor\$4)) and ((correct\$5 or adjust\$5) near5 circuit\$2)))	USPAT; EPO; JPO; DERWENT	2003/04/29 11:03
206	27	((light near5 (receive\$4 or receipt\$4 or detect\$4 or sense\$4)) and ((amplifier\$8 or preamplifier\$8 or AC) near8 (DC near3 bias))) and ((DC near5 (detect\$4 or sense\$4 or monitor\$4)) and ((correct\$5 or adjust\$5) near5 circuit\$2))	USPAT; EPO; JPO; DERWENT	2003/04/29 11:03
216	5396	(apply\$3 or application\$6 or control\$4 or adjust\$4) near8 (DC near3 bias)	USPAT; EPO; JPO; DERWENT	2003/04/29 11:11
221	4116	(data and clock) near5 (recover\$2 or circuit\$5)	USPAT; EPO; JPO; DERWENT	2003/04/29 11:14
226	19	((apply\$3 or application\$6 or control\$4 or adjust\$4) near8 (DC near3 bias)) and ((data and clock) near5 (recover\$2 or circuit\$5))	USPAT; EPO; JPO; DERWENT	2003/04/29 11:15